

FIG. 1

PRIOR ART

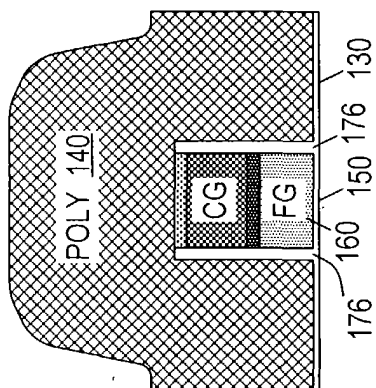
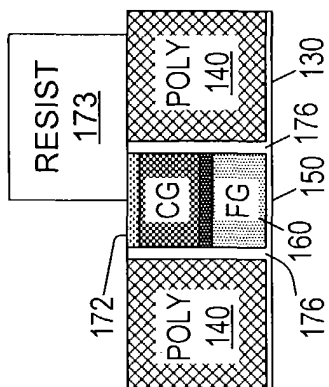


FIG. 2

PRIOR ART



3
G.
F

PRIOR ART

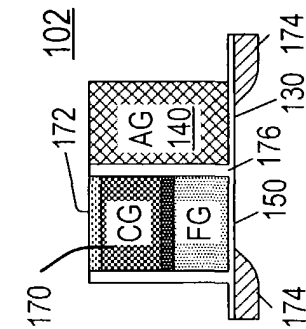


FIG. 4

PRIOR ART

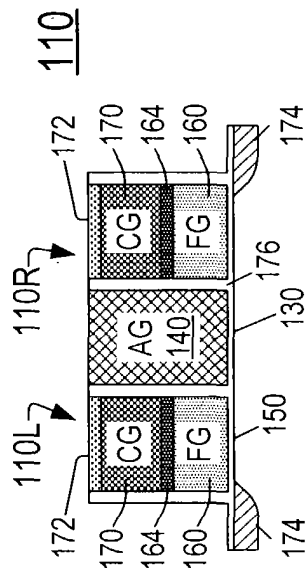
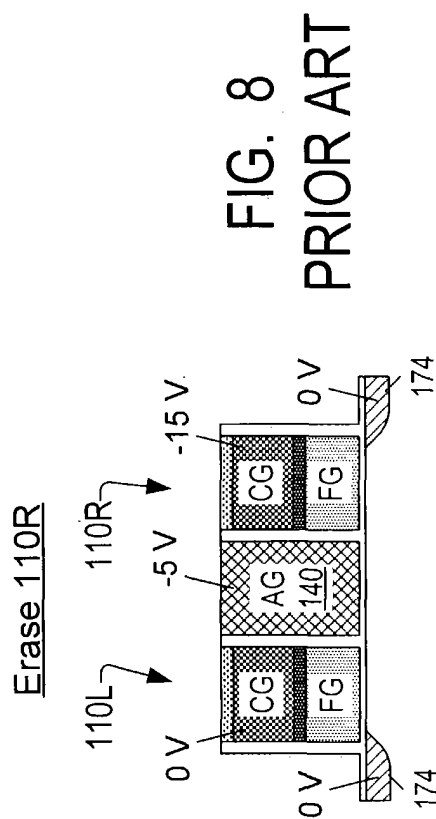
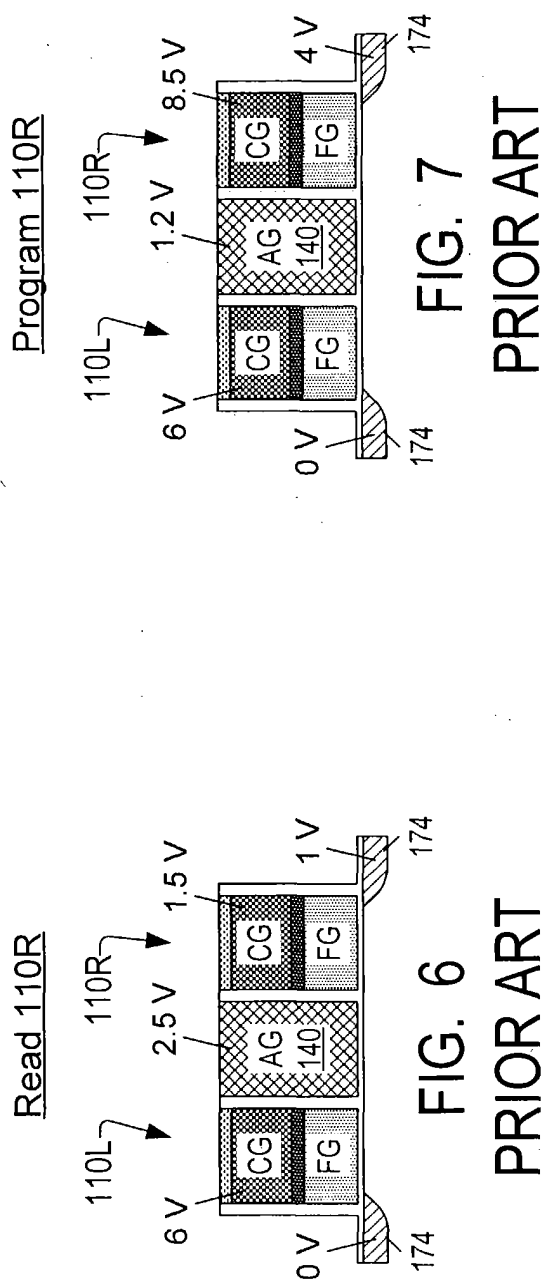


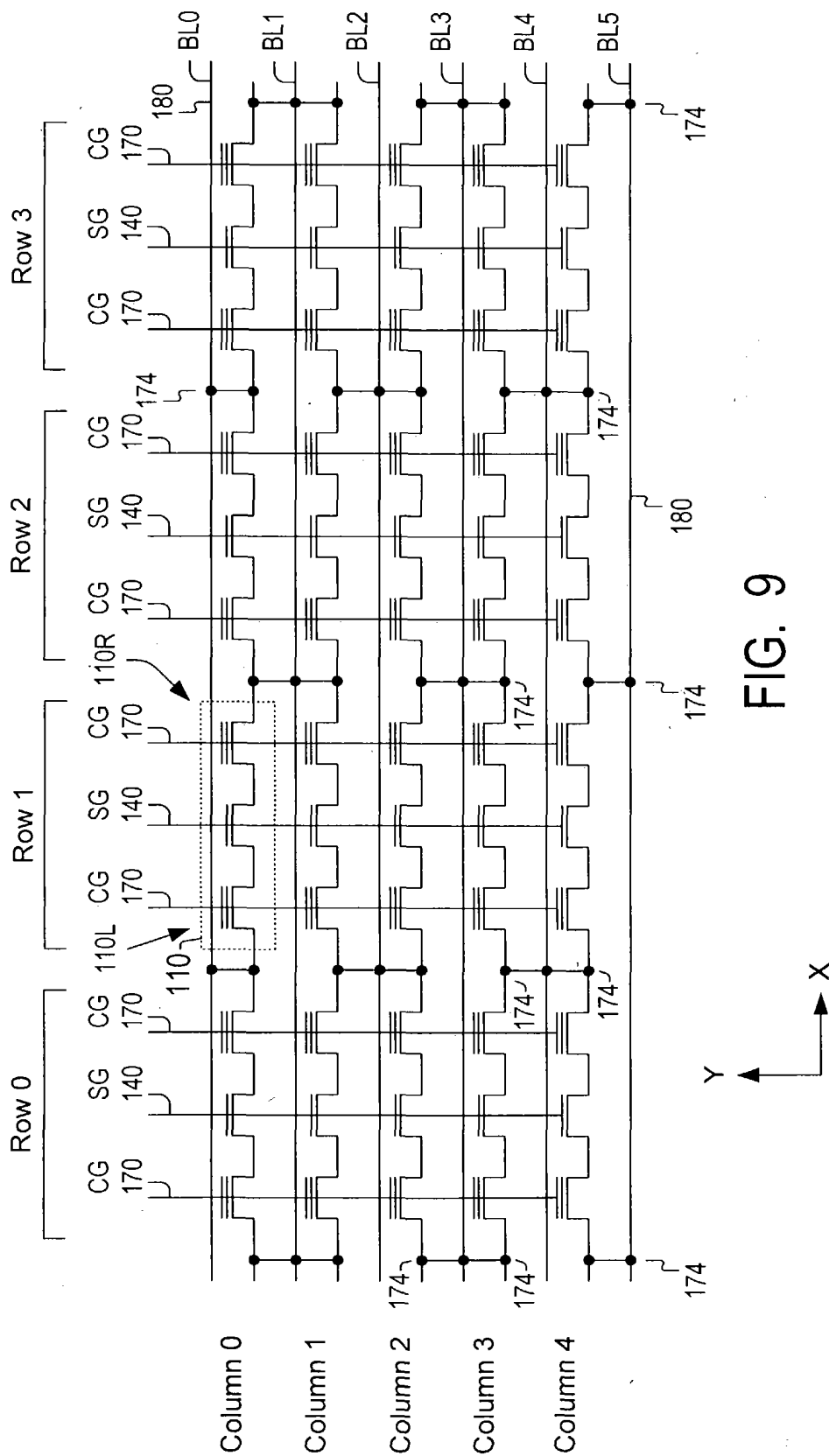
FIG. 5

PRIOR ART

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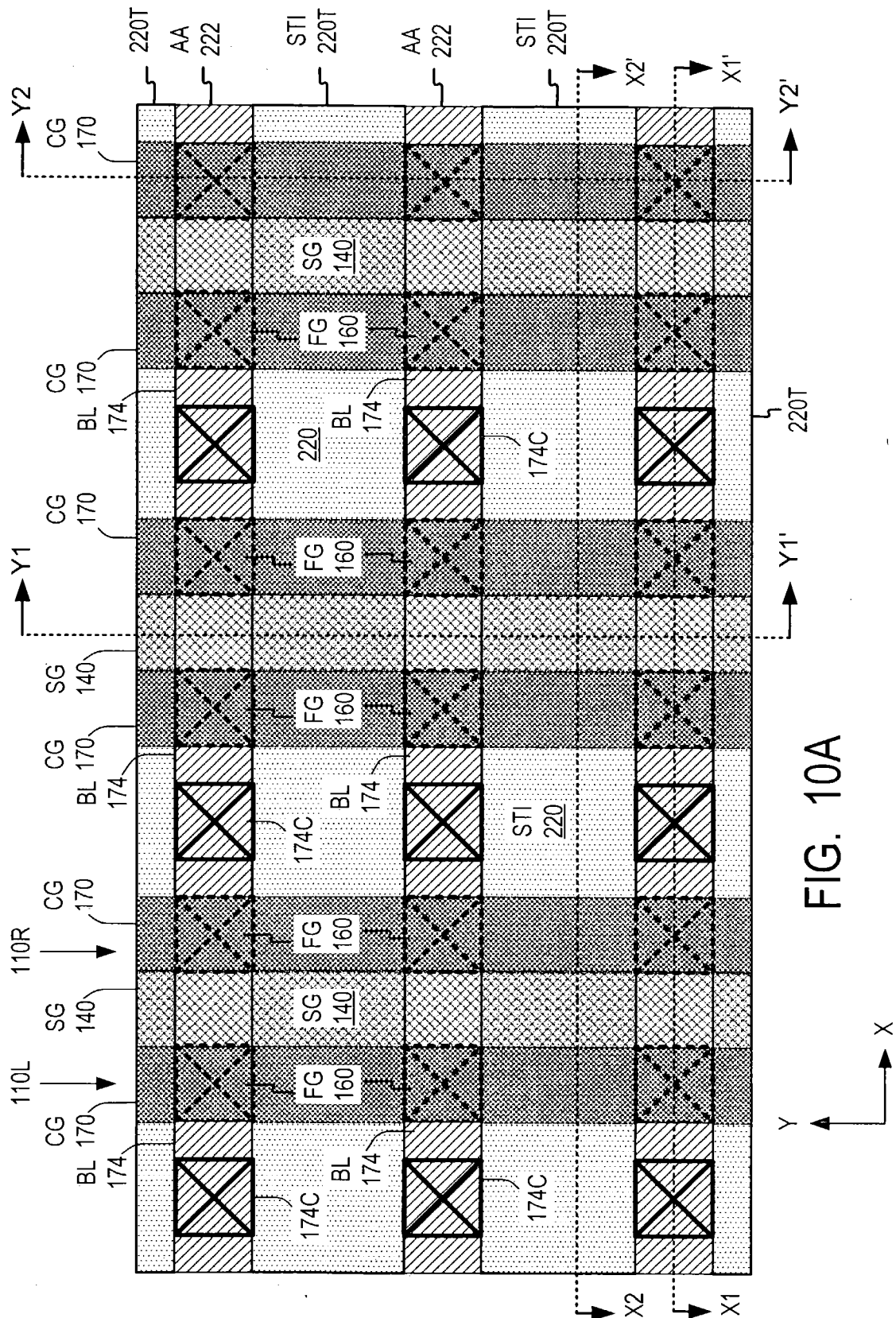
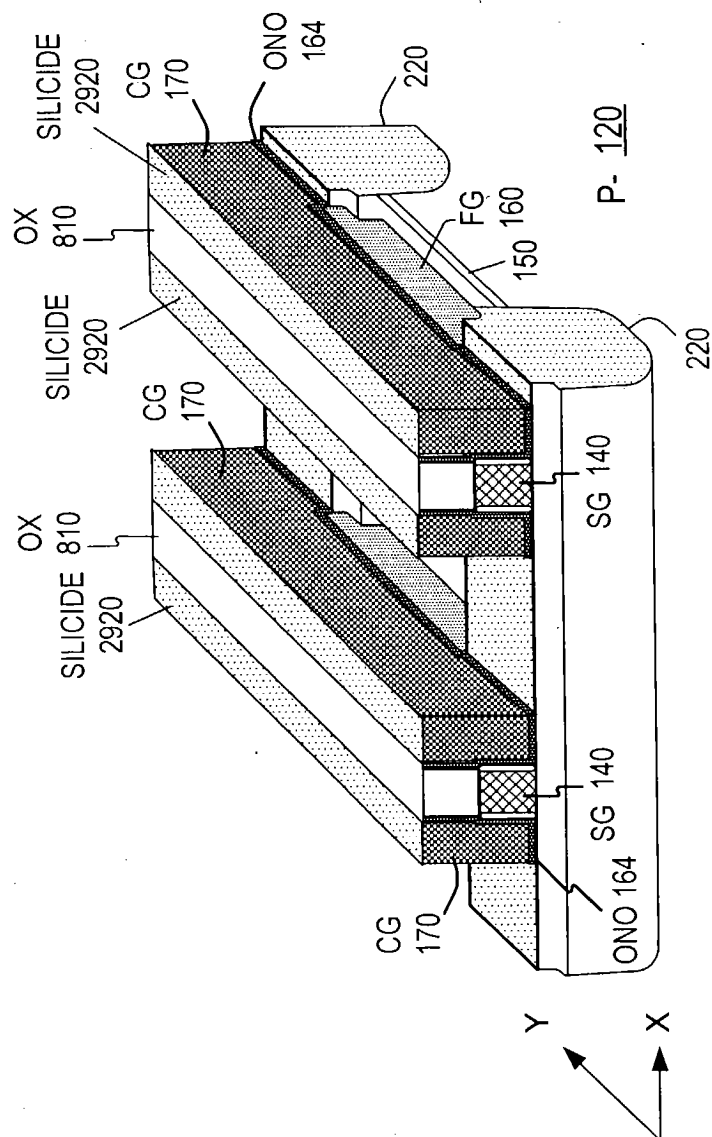


FIG. 10A

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FIG. 10B



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FIG. 11 (Y1-Y1')

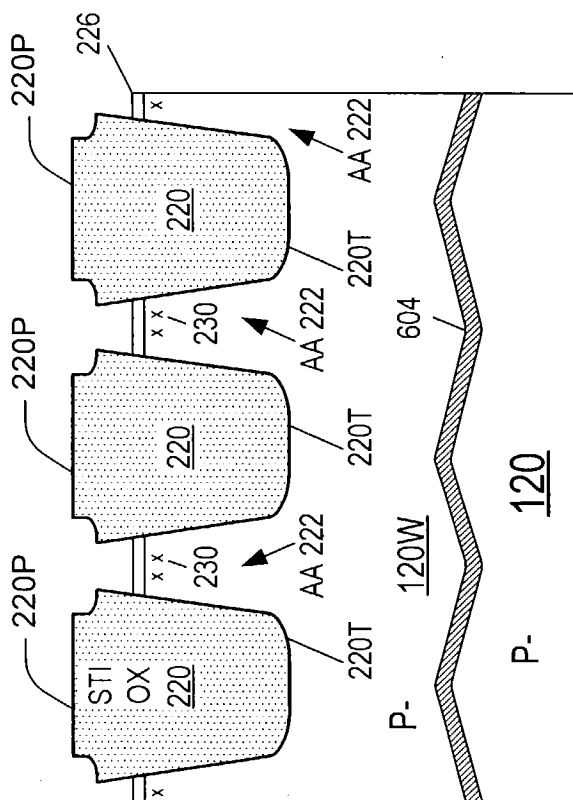


FIG. 12B
(PERIPHERY)

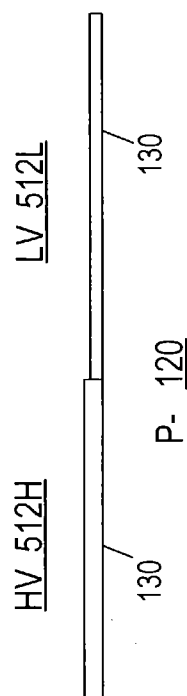
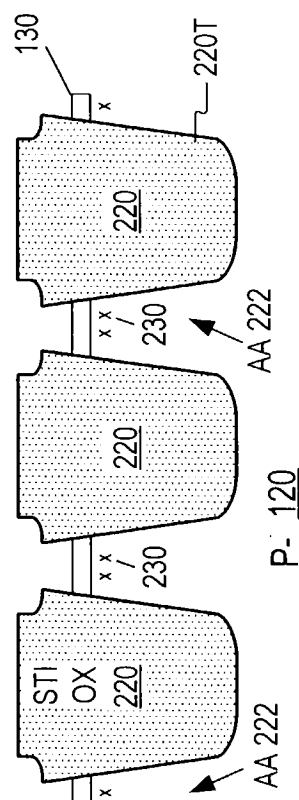


FIG. 12A (Y1-Y1')



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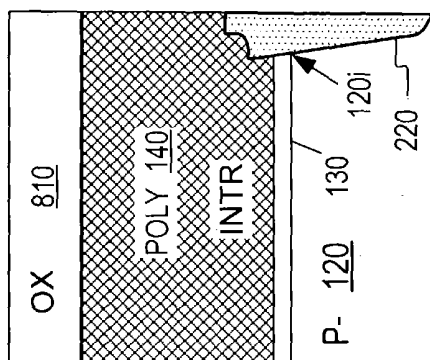


FIG. 13B
(PERIPHERY)

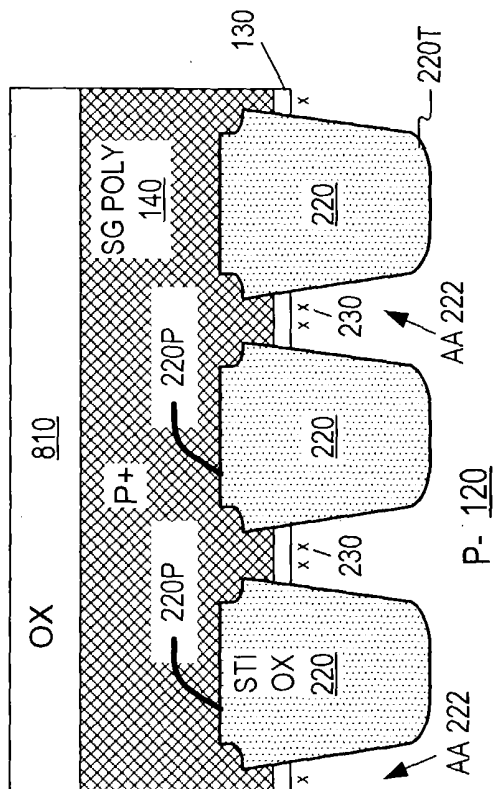


FIG. 13A (Y1-Y1')

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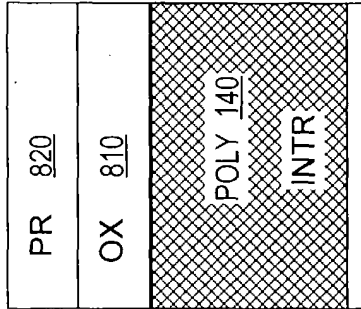


FIG. 14B
(PERIPHERY)

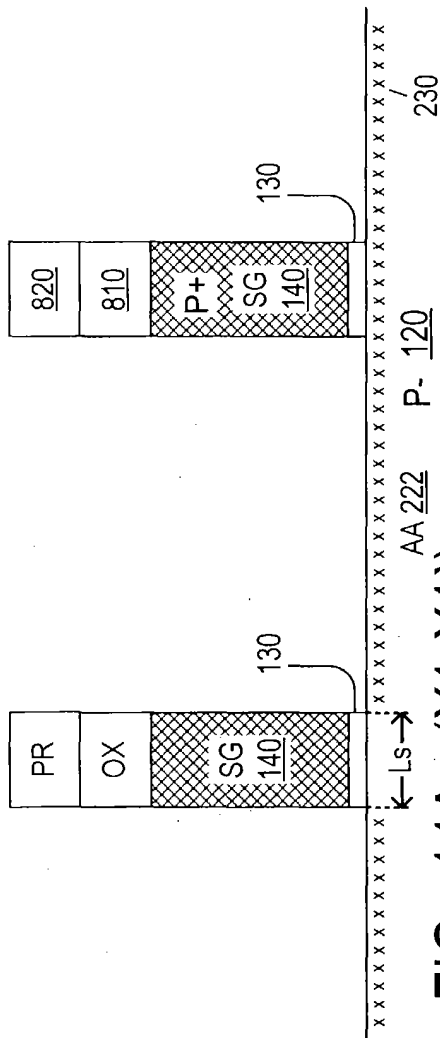


FIG. 14A (X1-X1')

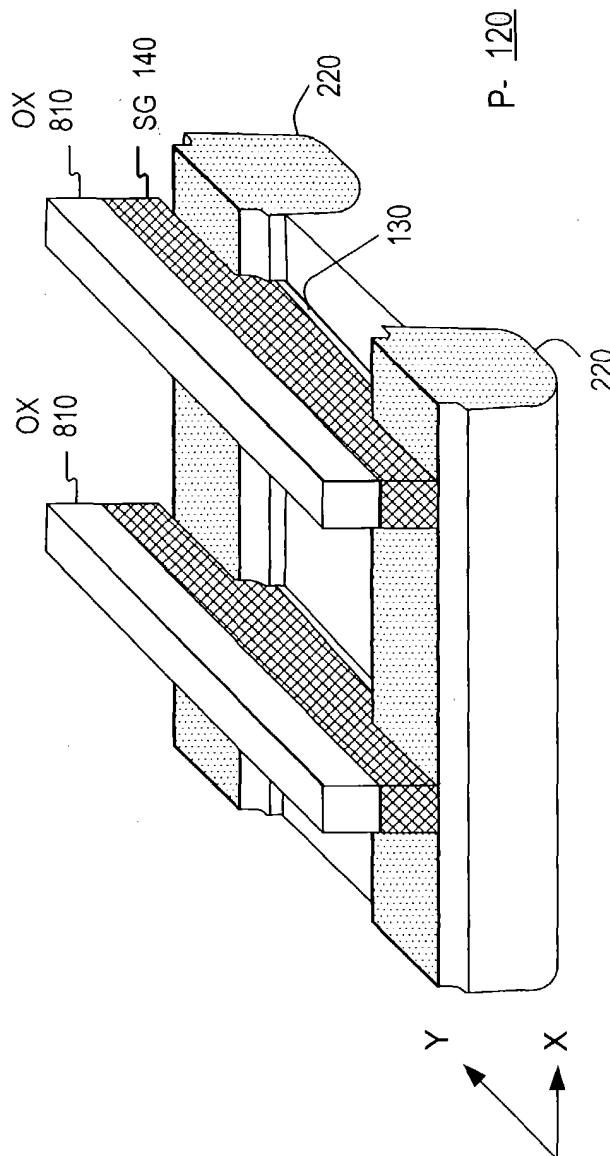


FIG. 15

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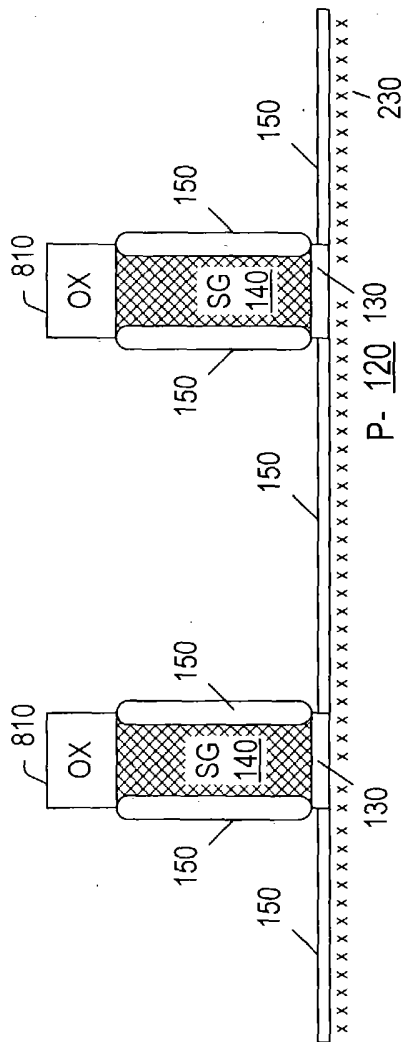


FIG. 16
(X1-X1'')

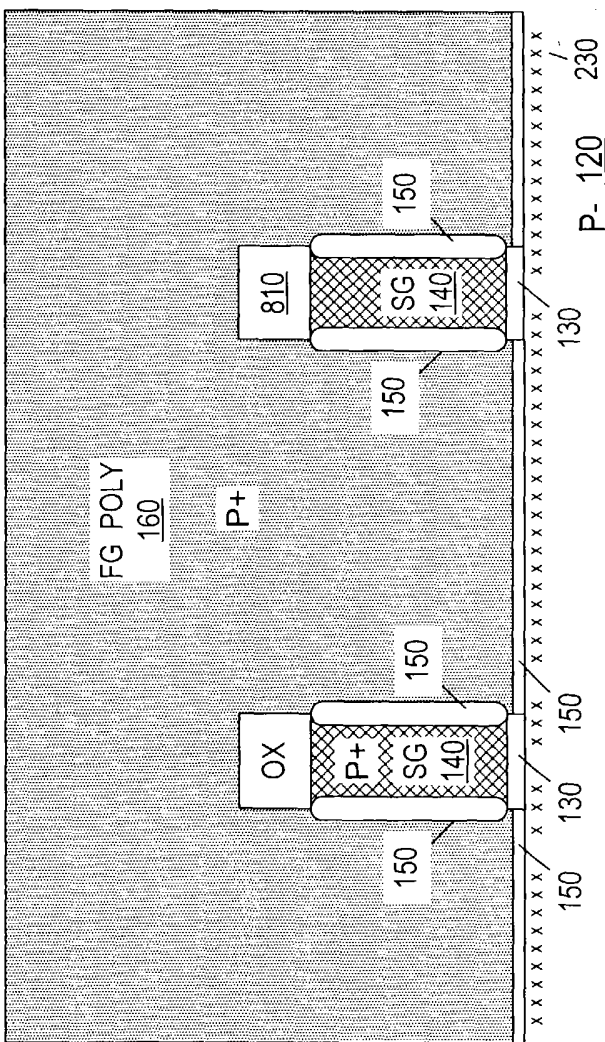


FIG. 17
(X1-X1'')

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FIG. 18
(X1-X1')

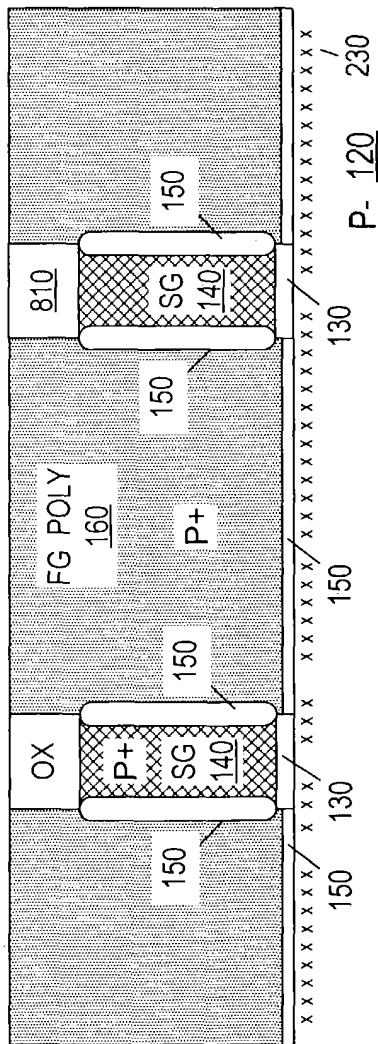


FIG. 19A
(X1-X1')

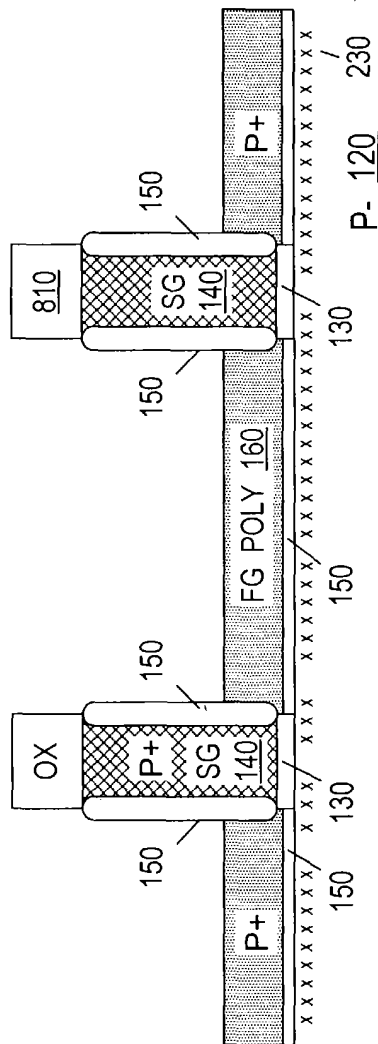
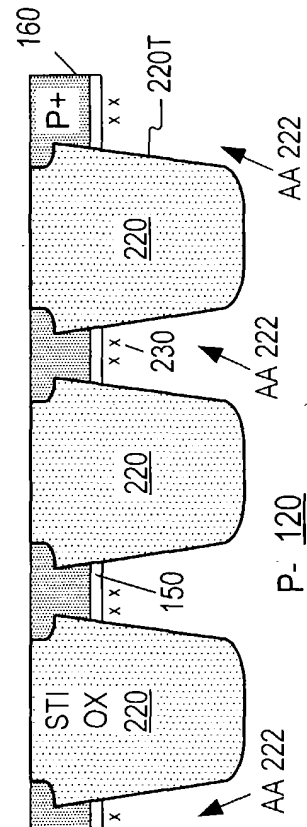
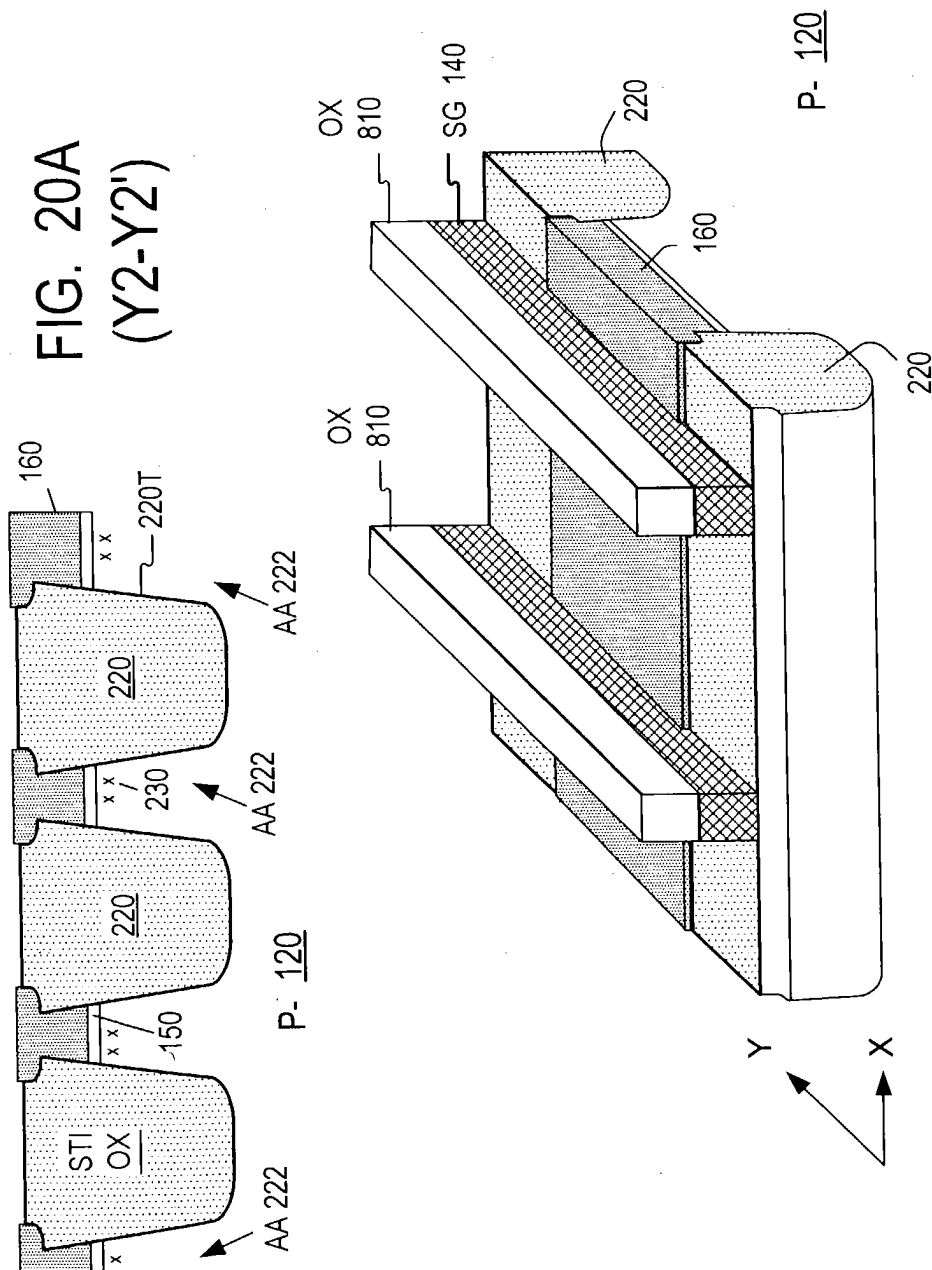


FIG. 19B
(Y2-Y2')



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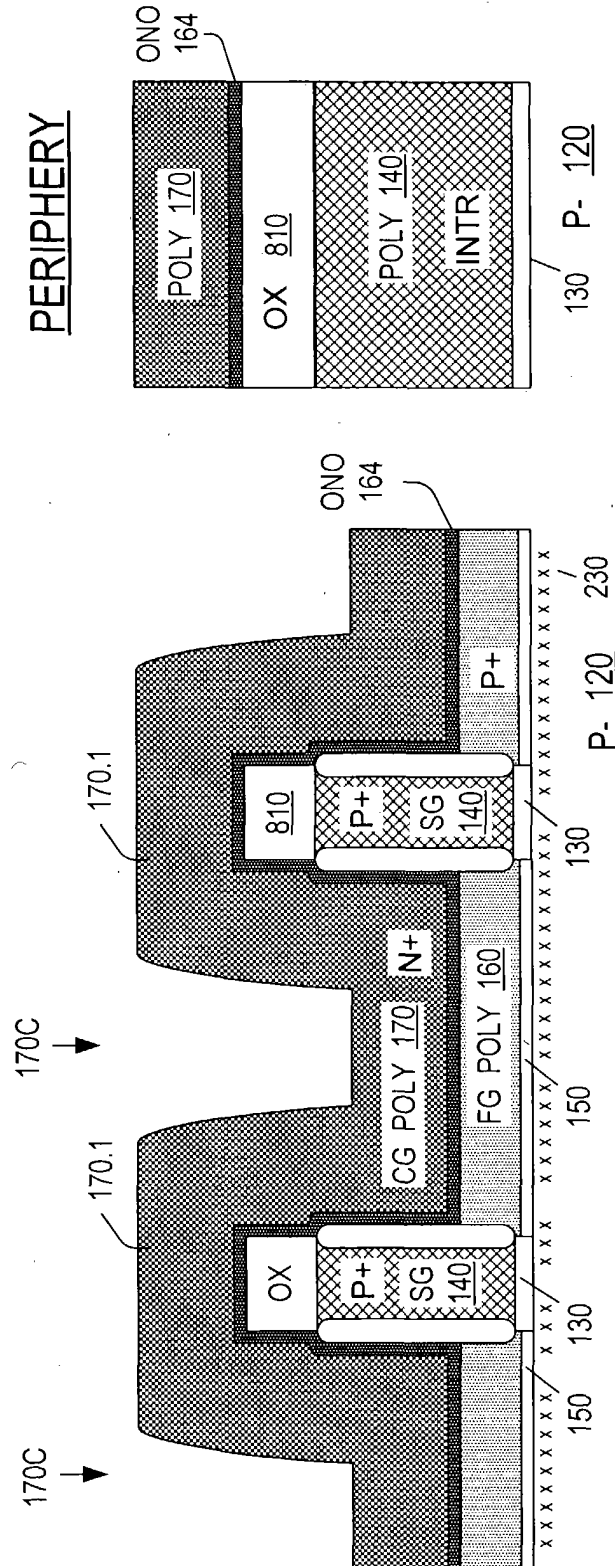


FIG. 21B

FIG. 21A (X1-X1')

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FIG. 22
(X1-X1')

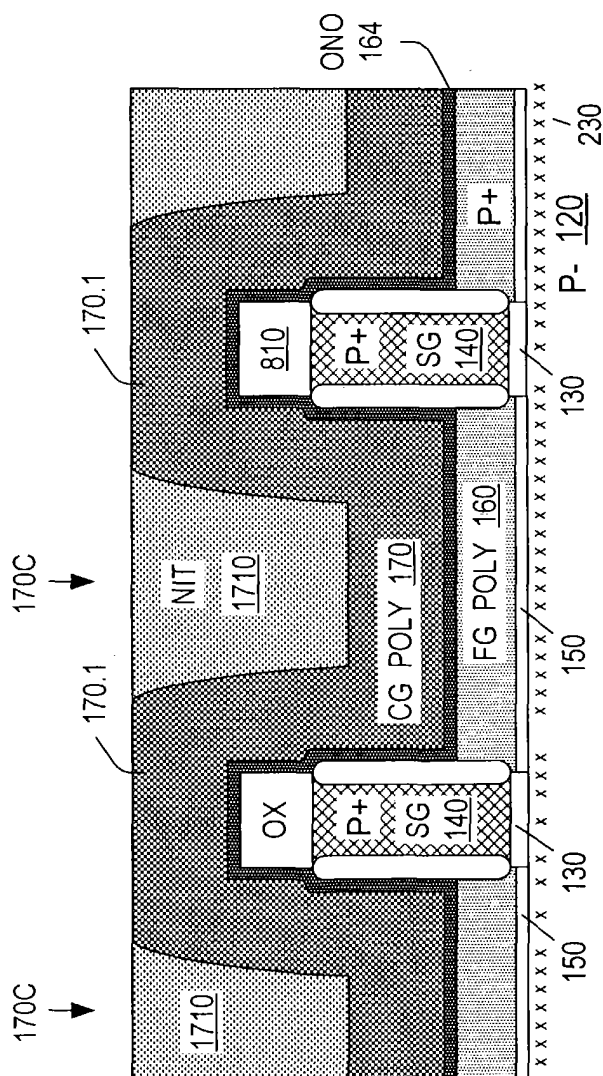


FIG. 23B

PERIPHERY

OX 810

POLY 140

INTR

160

164

130

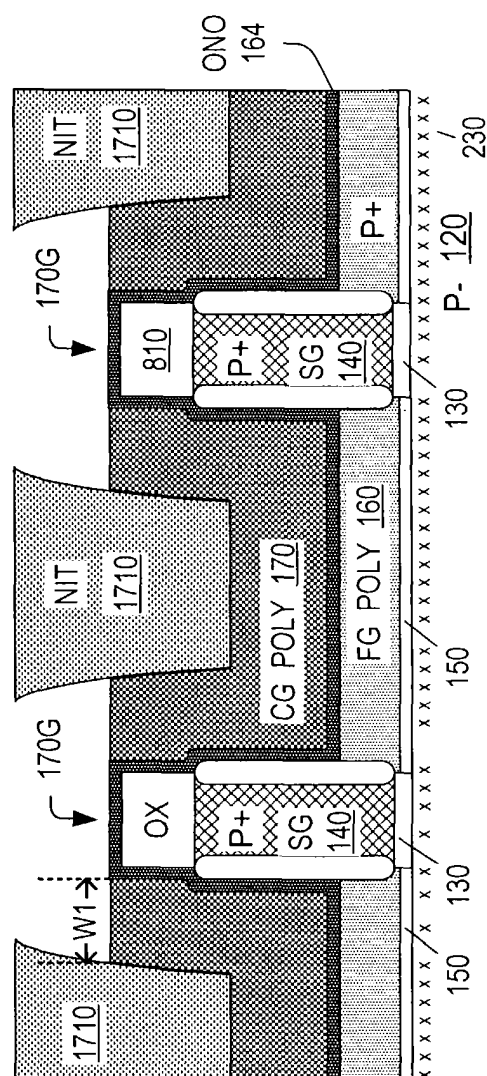


FIG. 23A (X1-X1')

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FIG. 24
(X1-X1')

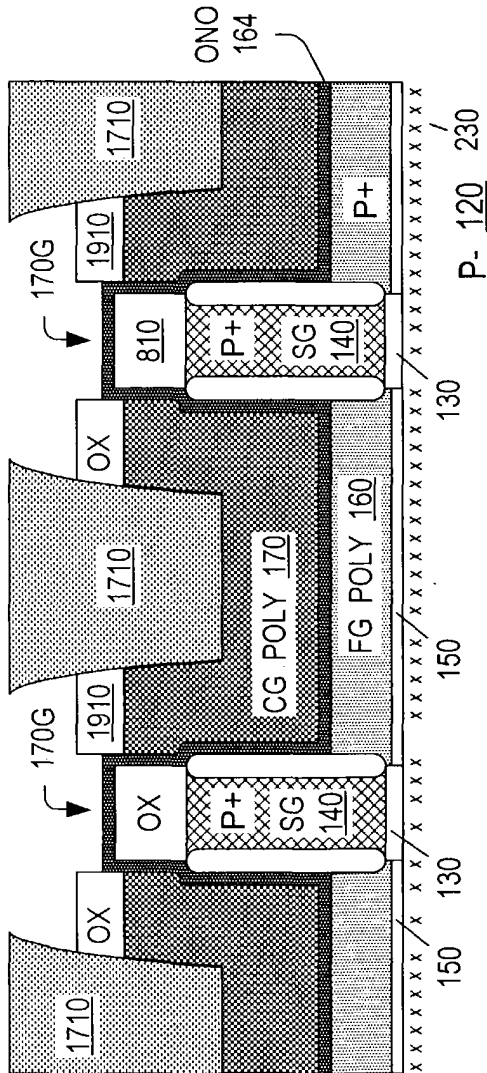
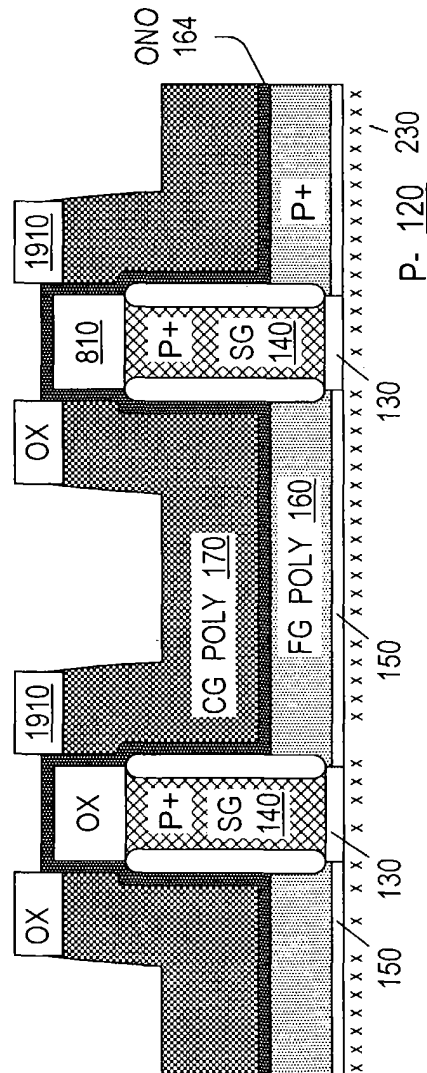


FIG. 25
(X1-X1')



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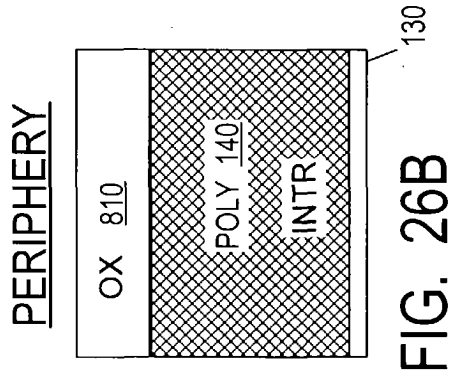
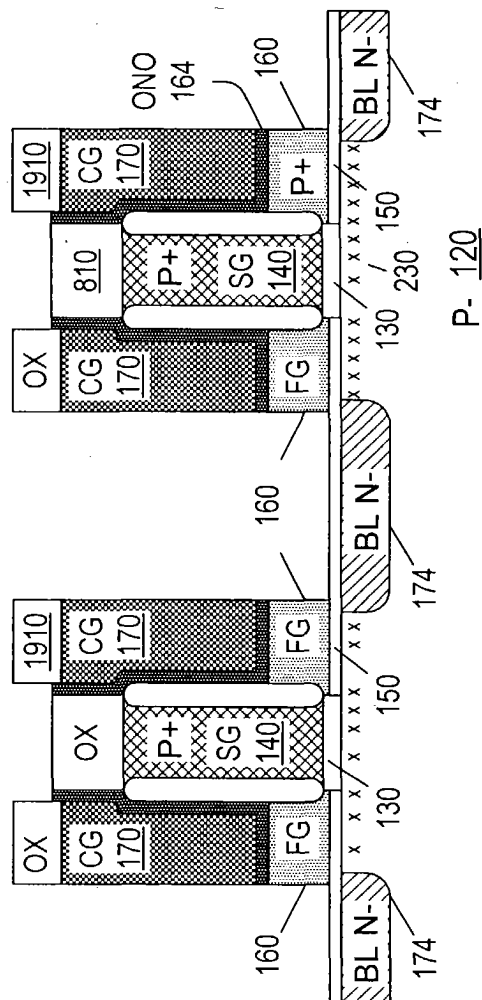
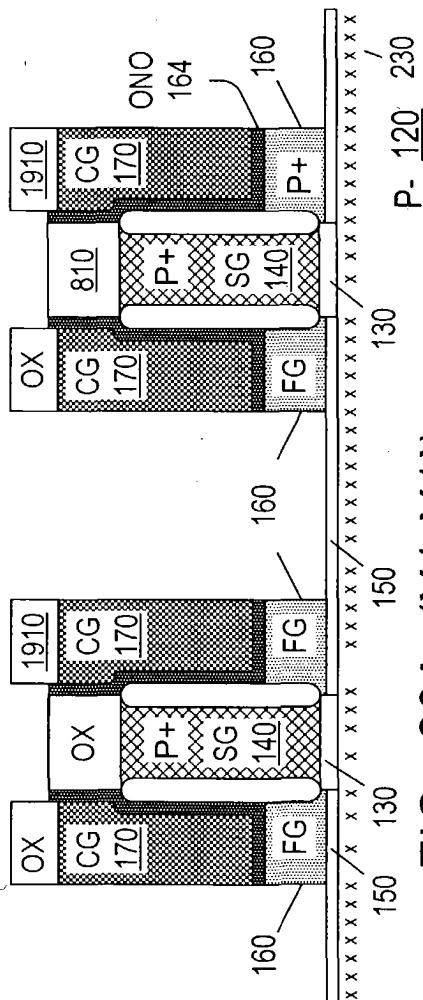


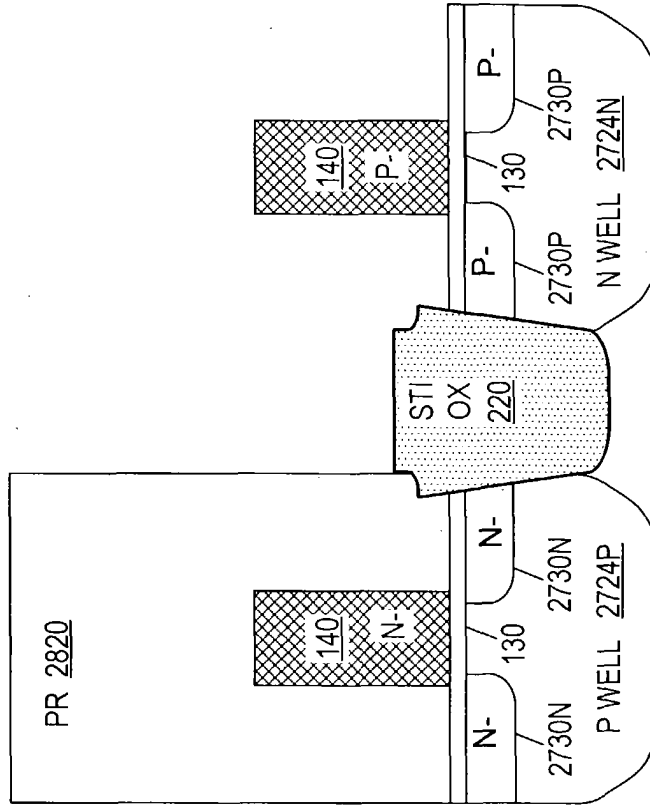
FIG. 27A
(X1-X1')



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PMOS 512P

NMOS 512N



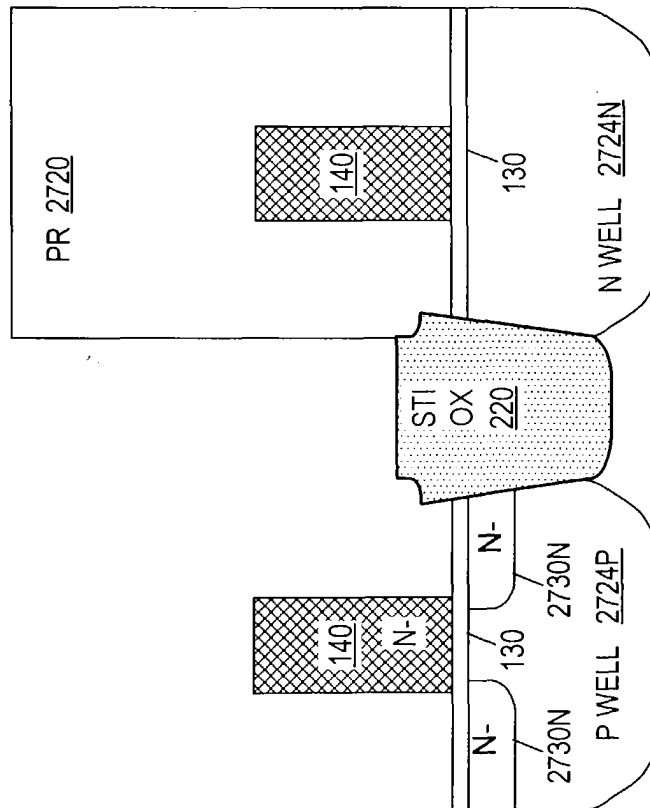
P- 120

FIG. 28

(PERIPHERY)

PMOS 512P

NMOS 512N

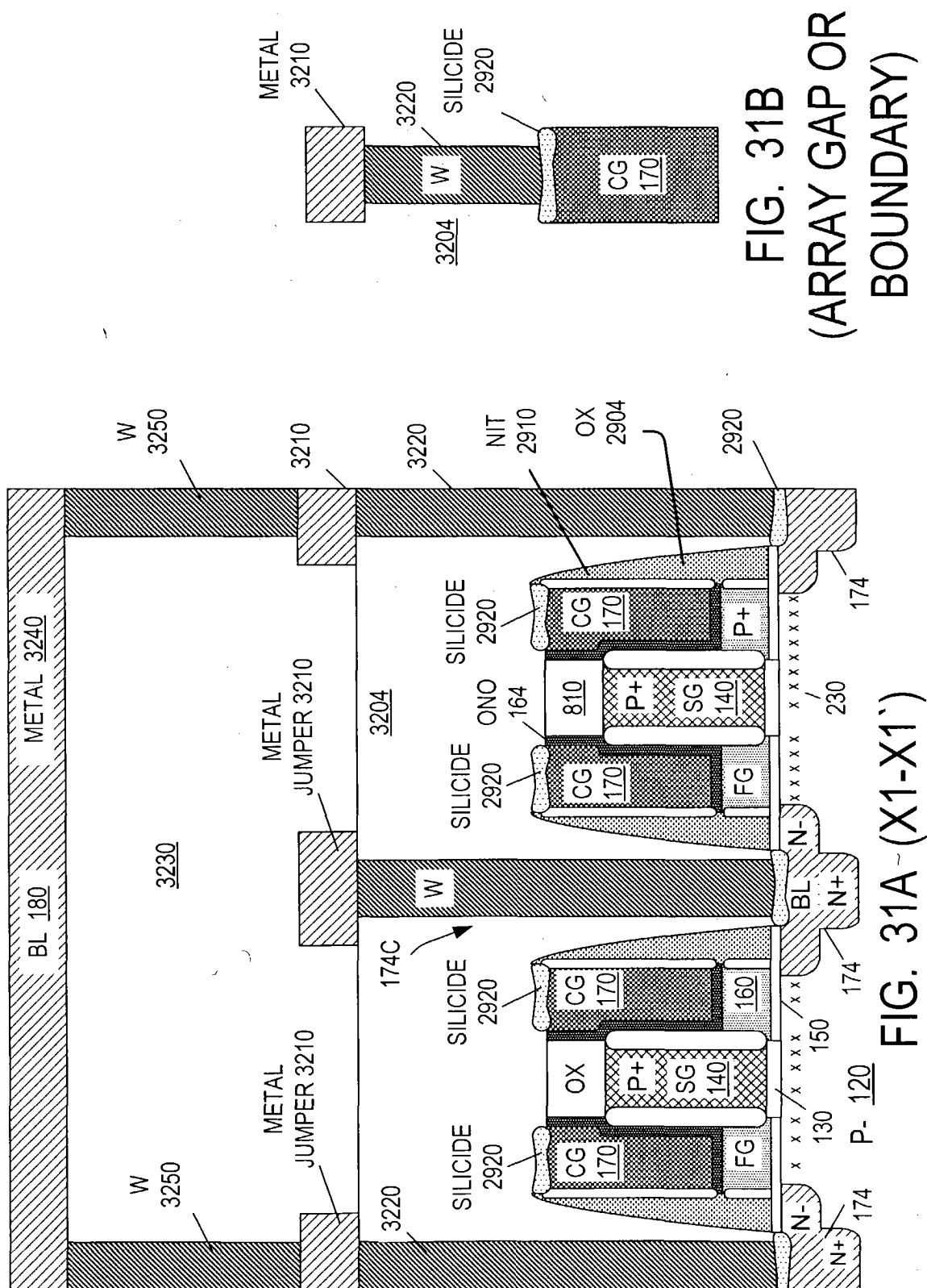


P- 120

FIG. 27B

(PERIPHERY)

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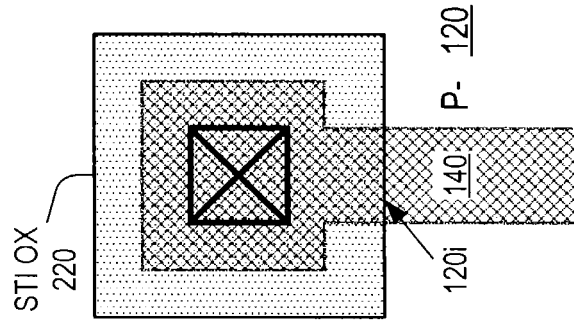


FIG. 31E
(PERIPHERY)

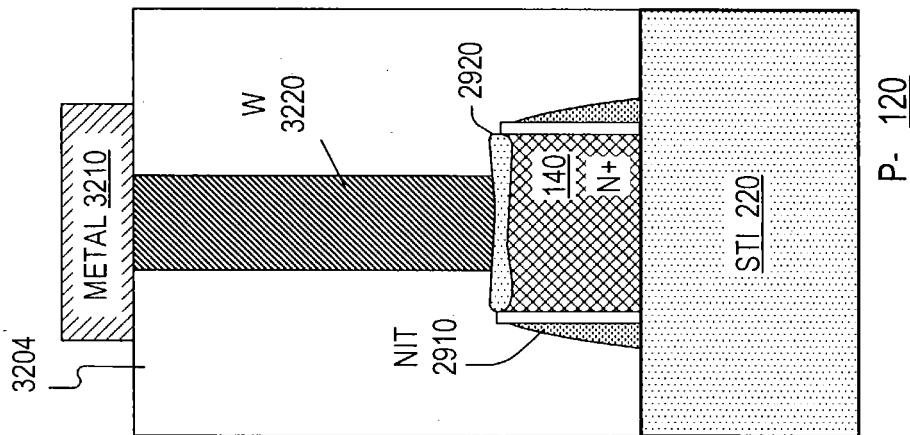


FIG. 31D
(PERIPHERY)

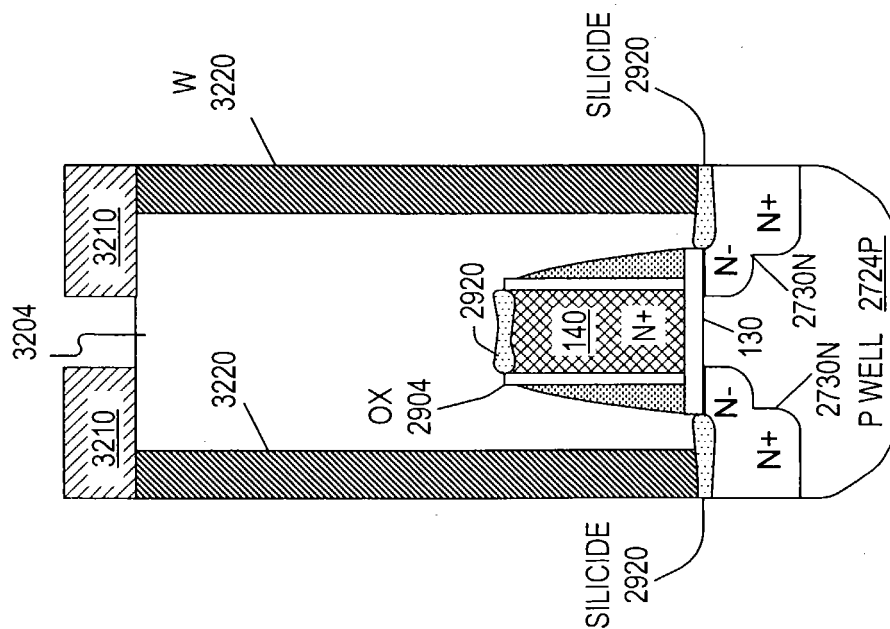


FIG. 31C
(PERIPHERY)

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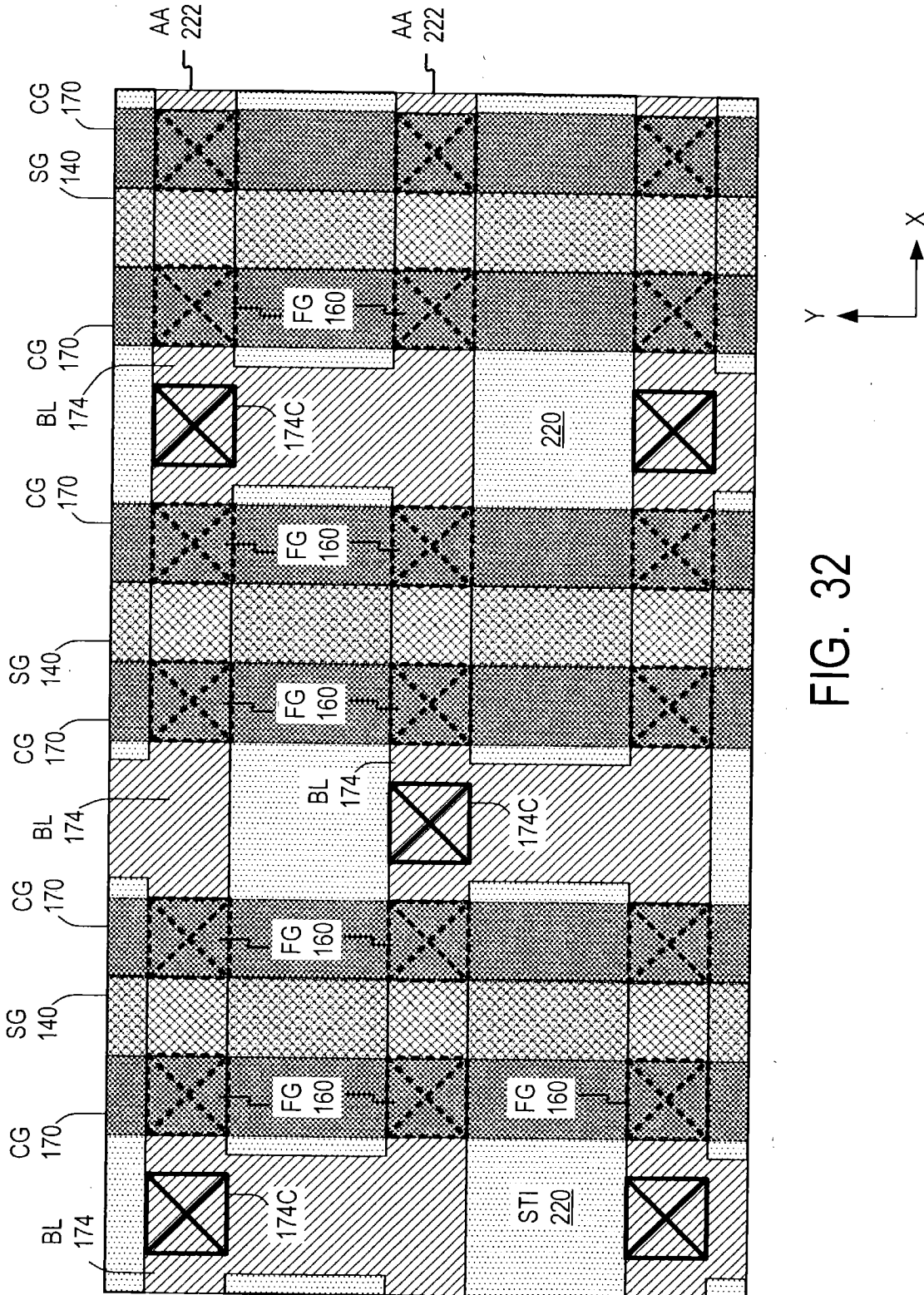


FIG. 32